

Figure 1

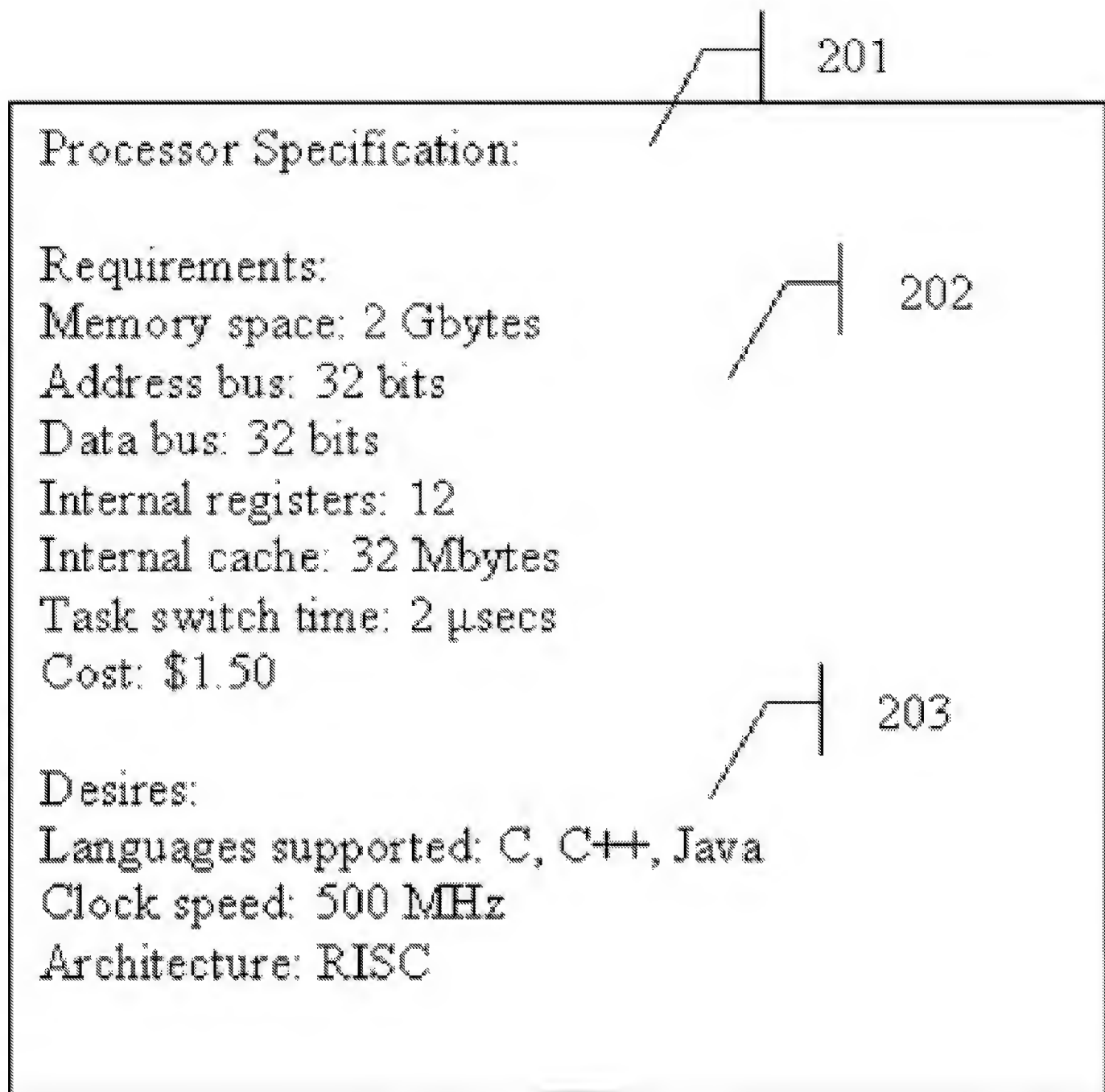


Figure 2

Processor	Memory space	Address bus	Data bus	Internal registers	Internal cache	Task switch time	Cost	Languages	Clock speed	Architecture
MIPS32 M4K	4 Gbytes	32 bits	32 bits	24	16 Mbytes	2 μ secs	\$54.00	C, C++, Java, asm	500 MHz	RISC
ARM7TDMI	4 Gbytes	32 bits	32 bits	16	16 Mbytes	3 μ secs	\$22.00	C, C++, Java, asm	300 MHz	RISC
PowerPC 405	4 Gbytes	32 bits	32 bits	16	32 Mbytes	4 μ secs	\$65.00	C, C++, Java, asm	300 MHz	RISC
68HC11	64 Kbytes	16 bits	8 bits	4	0 Mbytes	1 μ secs	\$1.00	asm	10 MHz	CISC
8051	64 Kbytes	16 bits	32 bits	4	0 Mbytes	1 μ secs	\$0.50	C, asm	10 MHz	CISC

Figure 3

```

    /*** This is the normal header comments for the routine      ***
    /***                                                         ***
    /***                                                         ***
    /***                                                         ***
    /*** The following information is required for the present    ***
    invention      ***
    /*** Driver information:
           Name: Ethernet driver
           Version: 1.01
           Hardware: ETH101
           Other:
    /***
    EtherDriver(in1, in2, in3, out);
    (
        // body of code
    )
  
```

Figure 4

600

```
/** This is the normal header comments for the routine *****/
```

```
/** *****/
```

```
/** *****/
```

```
/** *****/
```

```
/** The following information is required for the present  
invention *****/
```

```
/** Driver information:
```

```
    Name: USB driver
```

```
    Version: 2.50
```

```
    Hardware: Custom
```

```
    HDL file: USB.v
```

```
    Other:
```

```
*****/
```

```
USBDriver(in1, in2, in3, out);
```

```
{
```

```
    // body of code
```

```
}
```

601

602

603

Figure 5

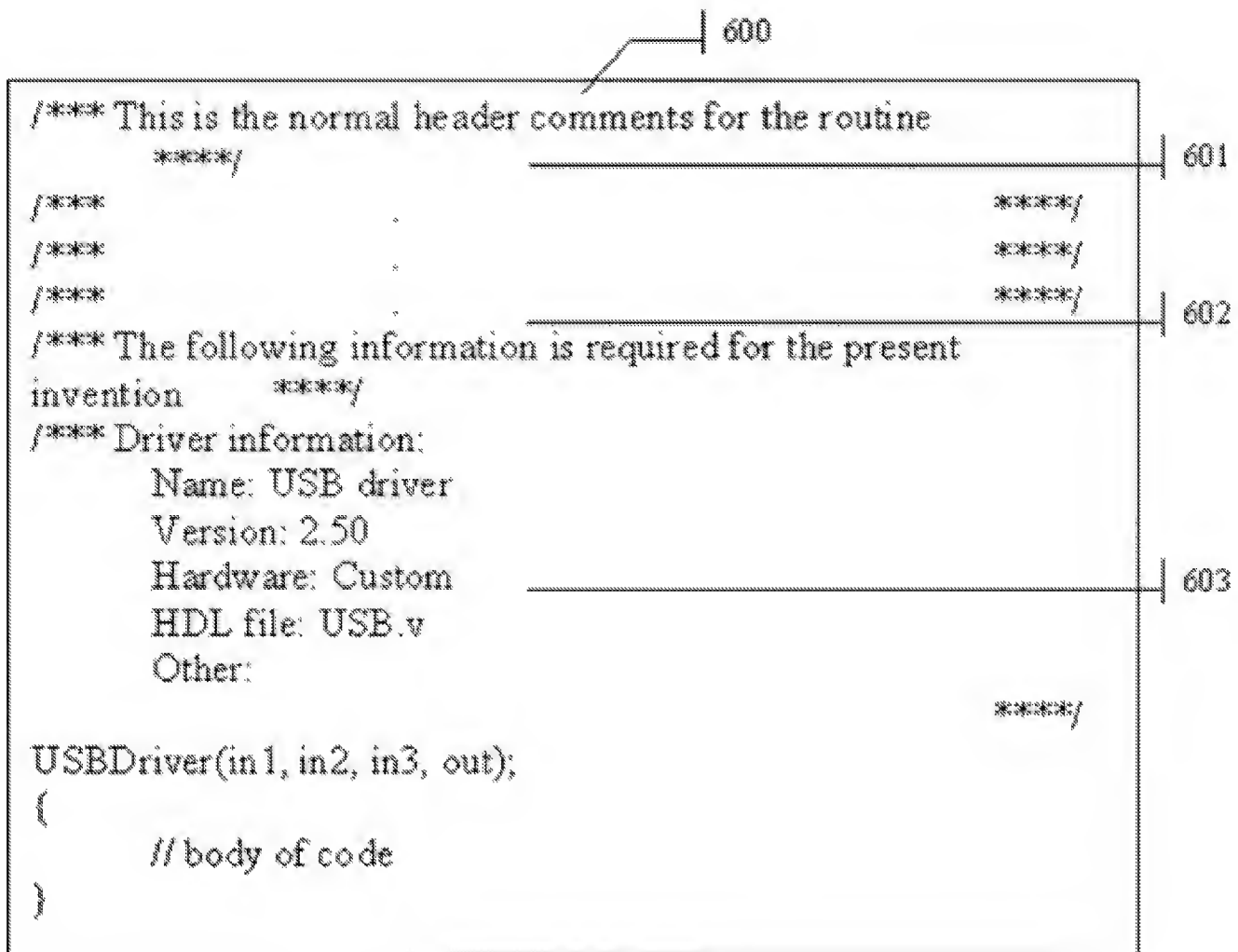


Figure 6

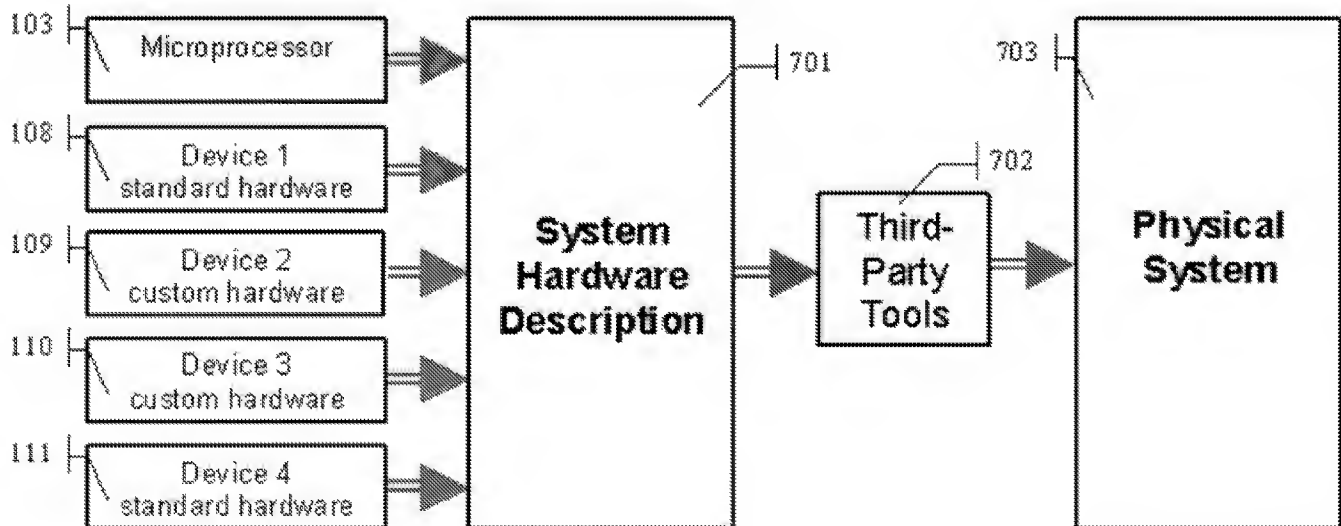


Figure 7

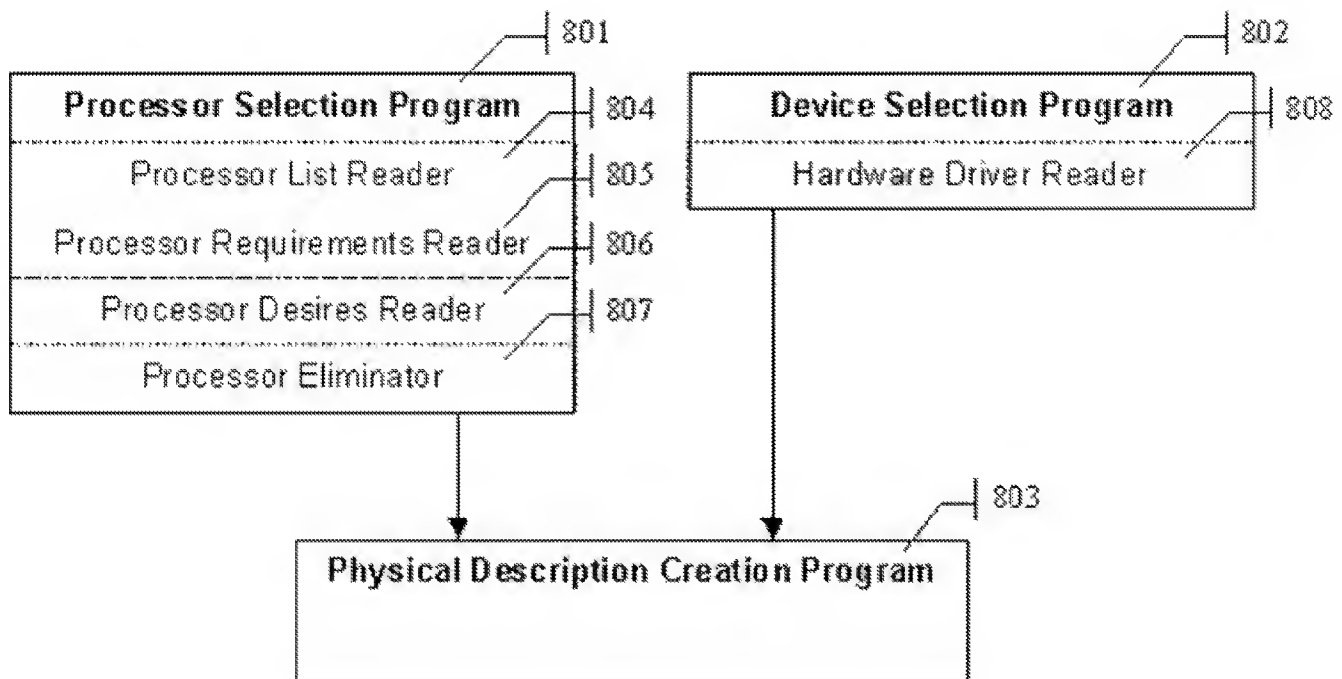


Figure 8

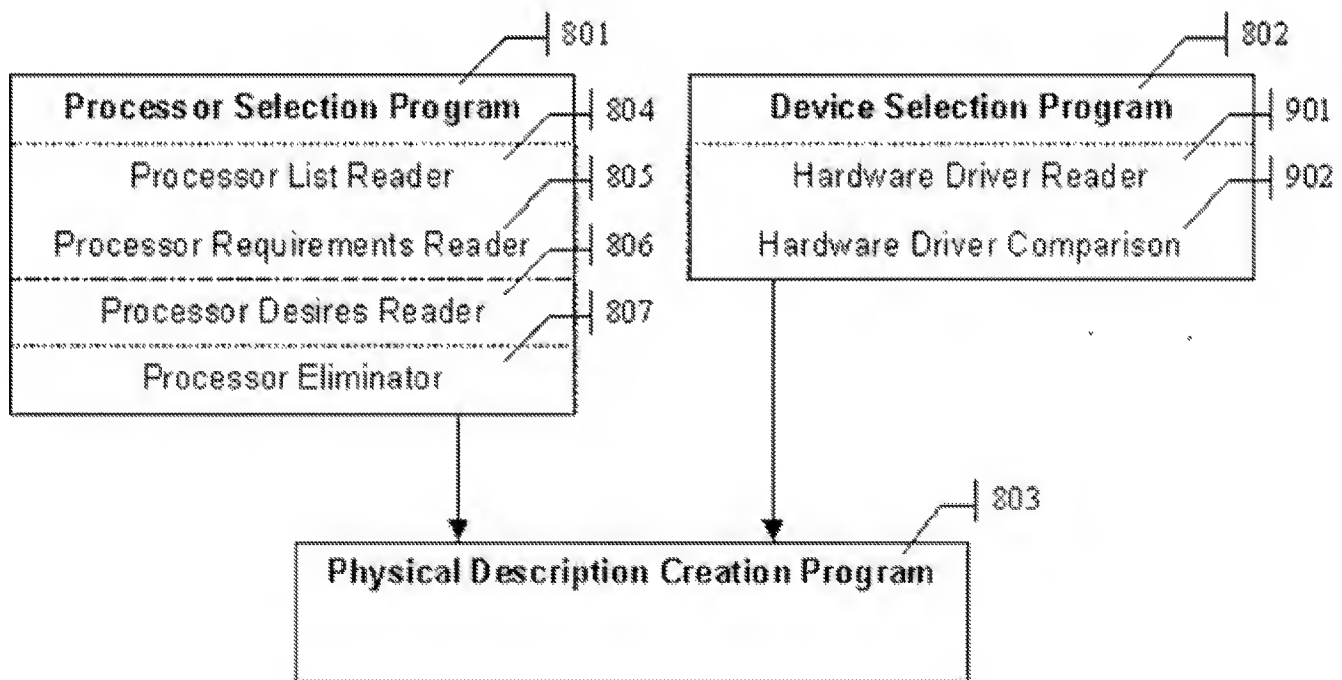


Figure 9